

**The University of Azad Jammu and Kashmir,**

**Muzaffarabad**

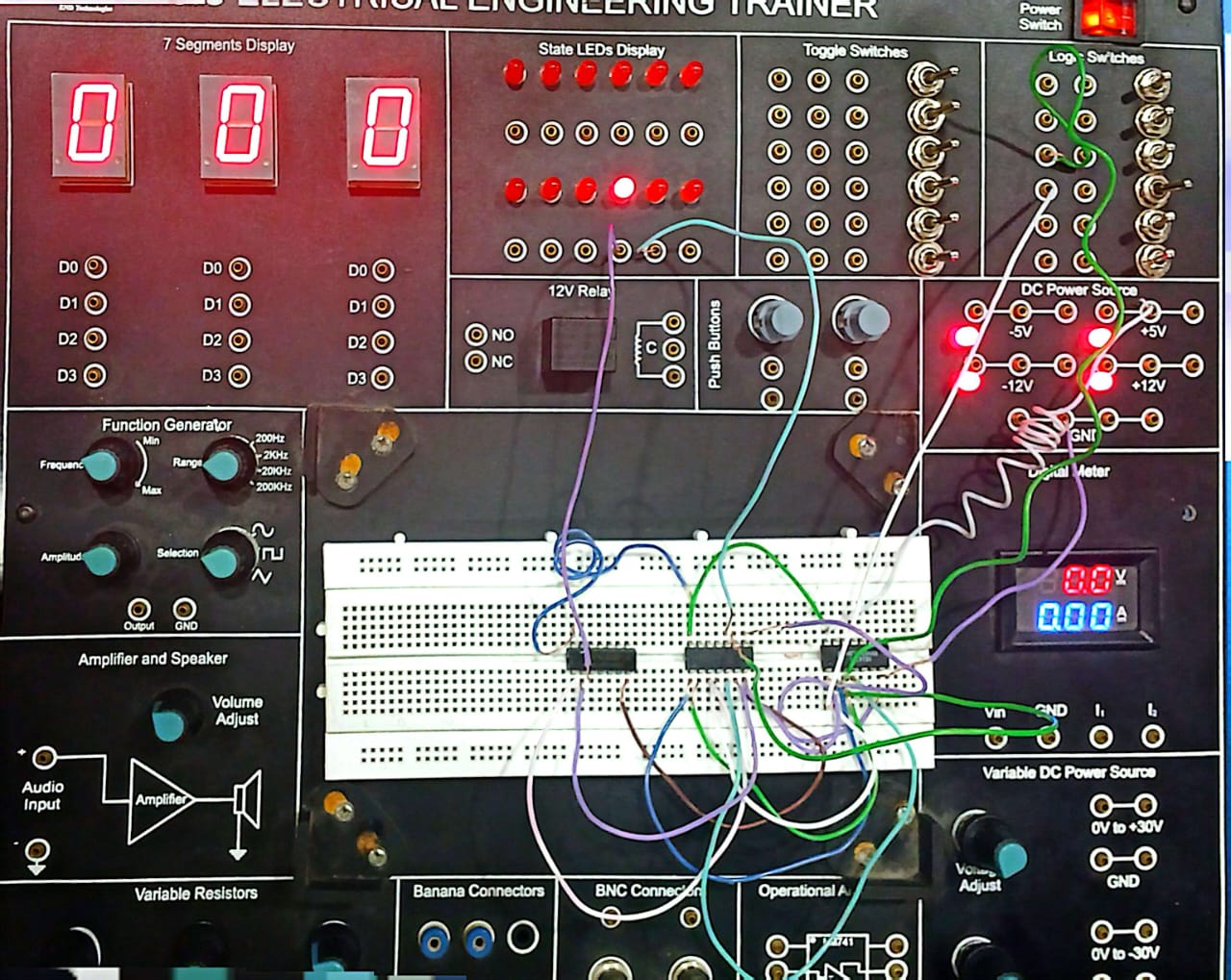
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| --- | --- |
| Name | Kamal Ali Akmal |
| Course Name | Computer Architecture and Logic Design |
| Submitted to | Engr. Sidra Rafique |
| Semester | 2nd |
| Session | 2024-2028 |
| Roll No | 2024-SE-38 |
| Lab No | 05 |

Department of Software Engineering

**Half Adder Not**

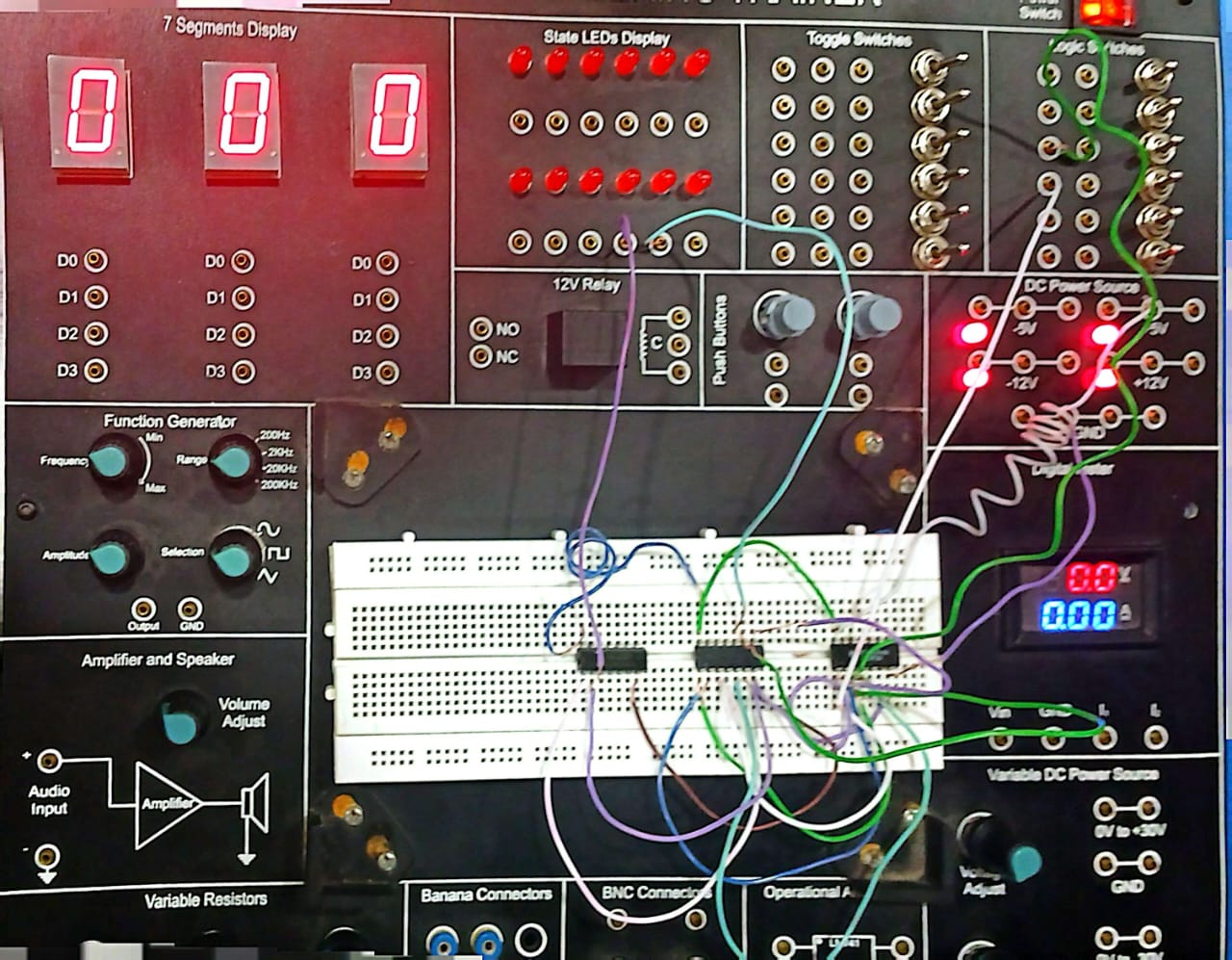
A B = Sum Carry

1 0 = 1 0



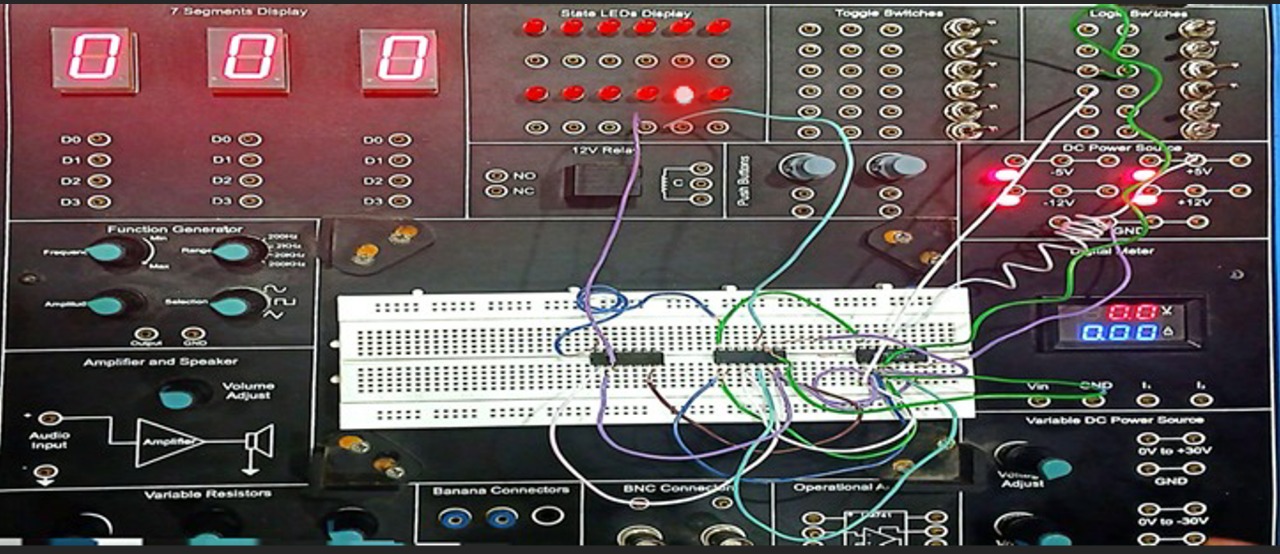
A B = Sum Carry

0 0 = 0 0



A B = Sum Carry

0 1 = 1 0



A B = Sum Carry

1 1 = 0 1



A close-up of a machine

AI-generated content may be incorrect.

**Half Adder X-OR**

A B = Sum Carry

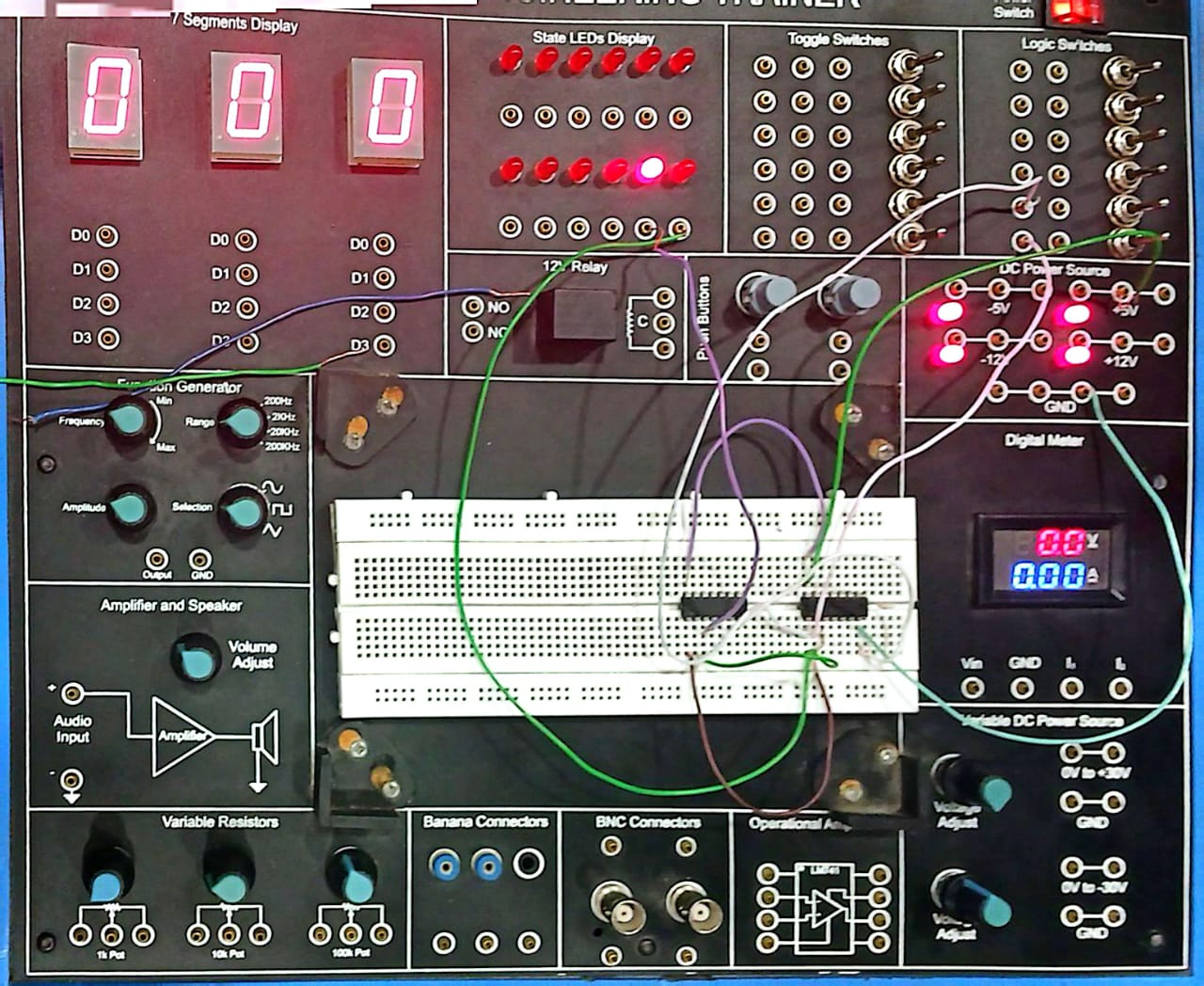
0 0 = 0 0

A close-up of a electrical trainer

AI-generated content may be incorrect.

A B = Sum Carry

1 1 = 0 1



A B = Sum Carry

0 1 = 1 0

